

\$ copy

PTO/SB/21 (09-04)

Approved for use through 07/31/2006. OMB 0651-0031
U.S. Patent and Trademark Office, U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/615,287
	Filing Date	July 7, 2003
	First Named Inventor	Kunal R. Parekh, et al.
	Art Unit	2812
	Examiner Name	H. Jey Tsai
Total Number of Pages in This Submission	Attorney Docket Number	MI22-2260

Certificate of Correction
MAR 17 2006

ENCLOSURES (Check all that apply)		
<input checked="" type="checkbox"/> Fee Transmittal Form <input checked="" type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment/Reply <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Reply to Missing Parts/ Incomplete Application <input type="checkbox"/> Reply to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation <input type="checkbox"/> Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s) _____ <input type="checkbox"/> Landscape Table on CD	<input type="checkbox"/> After Allowance Communication to TC <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosure(s) (please identify below): Return Receipt Postcard; Request for Certificate of Correction; Certificate of Correction (2)
Remarks		

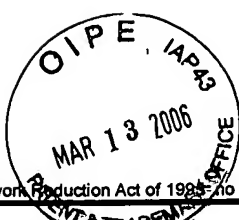
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT			
Firm Name	Wells St. John P.S.		
Signature			
Printed name	Mark S. Matkin		
Date	3/7/06	Reg. No.	32,268

CERTIFICATE OF TRANSMISSION/MAILING			
I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below:			
Signature			
Typed or printed name	Terri Lynn Boyd-Davis	Date	3-8-06

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

MAR 17 2006



PTO/SB/17 (01-06)

Approved for use through 07/31/2006. OMB 0651-0032

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number

Fees pursuant to the Consolidated Appropriations Act, 2005 (H.R. 4818).

FEE TRANSMITTAL
For FY 2006☐ Applicant claims small entity status. See 37 CFR 1.27

TOTAL AMOUNT OF PAYMENT (\$) 100.00

Complete if Known

Application Number	10/615,287
Filing Date	July 7, 2003
First Named Inventor	Kunal R. Parekh, et al.
Examiner Name	H. Jey Tsai
Art Unit	2812
Attorney Docket No.	MI22-2260

METHOD OF PAYMENT (check all that apply)☒ Check ☐ Credit Card ☐ Money Order ☐ None ☐ Other (please identify): _____☒ Deposit Account Deposit Account Number: 23-0925 Deposit Account Name: Wells St. John P.S.

For the above-identified deposit account, the Director is hereby authorized to: (check all that apply)

☐ Charge fee(s) indicated below ☐ Charge fee(s) indicated below, except for the filing fee☒ Charge any additional fee(s) or underpayments of fee(s) under 37 CFR 1.16 and 1.17 ☒ Credit any overpayments

WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.

FEE CALCULATION (All the fees below are due upon filing or may be subject to a surcharge.)**1. BASIC FILING, SEARCH, AND EXAMINATION FEES**

Application Type	FILING FEES		SEARCH FEES		EXAMINATION FEES		Fees Paid (\$)
	Fee (\$)	Small Entity Fee (\$)	Fee (\$)	Small Entity Fee (\$)	Fee (\$)	Small Entity Fee (\$)	
Utility	300	150	500	250	200	100	
Design	200	100	100	50	130	65	
Plant	200	100	300	150	160	80	
Reissue	300	150	500	250	600	300	
Provisional	200	100	0	0	0	0	

2. EXCESS CLAIM FEES**Fee Description**

Each claim over 20 (including Reissues)

Fee (\$)	Small Entity Fee (\$)
50	25

Each independent claim over 3 (including Reissues)

200	100
-----	-----

Multiple dependent claims

360	180
-----	-----

Total Claims	Extra Claims	Fee (\$)	Fee Paid (\$)
--------------	--------------	----------	---------------

- 20 or HP = _____ x _____ = _____

HP = highest number of total claims paid for, if greater than 20.

Indep. Claims	Extra Claims	Fee (\$)	Fee Paid (\$)
---------------	--------------	----------	---------------

- 3 or HP = _____ x _____ = _____

HP = highest number of independent claims paid for, if greater than 3.

3. APPLICATION SIZE FEE

If the specification and drawings exceed 100 sheets of paper (excluding electronically filed sequence or computer listings under 37 CFR 1.52(e)), the application size fee due is \$250 (\$125 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).

Total Sheets	Extra Sheets	Number of each additional 50 or fraction thereof	Fee (\$)	Fee Paid (\$)
--------------	--------------	--	----------	---------------

- 100 = _____ / 50 = _____ (round up to a whole number) x _____ = _____

4. OTHER FEE(S)


Non-English Specification, \$130 fee (no small entity discount)

Fees Paid (\$)

Other (e.g., late filing surcharge): Certificate of Correction

100.00

SUBMITTED BY

Signature		Registration No. 32,268 (Attorney/Agent)	Telephone (509)624-4276
Name (Print/Type)	Mark S. Malen		Date 3-7-06

This collection of information is required by 37 CFR 1.136. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 30 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

MAR 17 2006



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No. 6,921,692 ^{B2}
Patent Issue Date July 26, 2005
Application Serial No. 10/615,287
Filing Date July 7, 2003
Assignee Micron Technology, Inc.
Inventorship Kunal R. Parekh, et al.
Attorney's Docket No. MI22-2260
Title: Methods of Forming Memory Circuitry

REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT FOR
APPLICANT MISTAKE and PTO MISTAKES (37 C.F.R. §§ 1.322(a) and 1.323)

To: Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
ATTN: Decision and Certificate of Correction
Branch of the Patent Issue Division

From: Mark S. Matkin (Tel. 509-624-4276; Fax 509-838-3424)
Wells St. John P.S.
601 W. First Avenue, Suite 1300
Spokane, WA 99201-3828

Sir:

It is hereby requested that a Certificate of Correction be issued with respect to Patent No. 6,921,692, granted July 26, 2005, in accordance with the Certificate of Correction form attached hereto in duplicate.

It is noted that an error appears in this patent of a typographical nature of character, as more fully described below. The error occurred in good faith. Correction thereof does not involve such changes in the patent as would constitute new matter or would require re-examination.

03/14/2006 SDENBOB1 00000014 6921692
01 FC:1811 100.00 DP

MAR 17 2006

Other errors listed on the Certificate of Correction form were apparently incurred through the fault of the PTO as will be disclosed by the records of files in the Office.

Attached hereto, in duplicate, is Form PTO-1050, with at least one copy being suitable for printing.

The exact page and line number where the error occurs in the application file is:


Page 12, paragraph [0029] in the Specification/Col. 4, line 36 in the Issued Patent.

Enclosed is a check in the amount of \$100.00, as required by 37 CFR 1.20(a).

Respectfully submitted,

Dated: 3-7-06

By:


Mark S. Matkin
Reg. No. 32,268

MAR 17 2006

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

Page 1 of 2

PATENT NO. : 6,921,692 *B2*
APPLICATION NO.: 10/615,287
DATED : July 26, 2005
INVENTOR(S) : Kunal R. Parekh, et al.

It is certified that an error or errors appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Cited Prior Art

In the second column, cover page, the 20th case cited:
Delete "6,458,859 B2 10/2002 Hayashida et al."
And insert --6,468,859 B2 10/2002 Hayashida et al.--

In the Specification

Col. 4, line 36 –
Replace "of example only, layer 54 might be formed to to provide a"
With --of example only, layer 54 might be formed to provide a--

Col. 4, line 40 –
Replace "layer 54 include conductive metal suicides (whether"
With --layer 54 include conductive metal silicides (whether--

In the Claims

Col. 7, line 17 -
Replace "The method of claim 13 wherein the sacrificial"
With --The method of claim 17 wherein the sacrificial--

Col. 7, line 60 -
Replace "plugging material with in the bit node contact opening"
With --plugging material within the bit node contact opening--

Mailing Address of Sender:
Mark S. Matkin
Wells St. John P.S.
601 West First Avenue, Suite 1300
Spokane, WA 99201-3828

MAR 17 2006

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

Page 2 of 2

PATENT NO. : 6,921,692 *B2*
APPLICATION NO.: 10/615,287
DATED : July 26, 2005
INVENTOR(S) : Kunal R. Parekh, et al.

It is certified that an error or errors appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 7, line 66 -

Replace "The method of claim 23 wherein the forming or the"
With --The method of claim 23 wherein the forming of the--

Col. 9, line 52 -

Replace "plagiarizing sacrificial plugging material."
With --planarizing sacrificial plugging material.--

Col. 10, line 13 -

Replace "plagiarizing sacrificial plugging material."
With --planarizing sacrificial plugging material.--

Col. 10, line 27 -

Replace "outer material Selectively to the etch stop material."
With --outer material selectively to the etch stop material.--

Col. 10, line 47 -

Replace "plagiarizing sacrificial plugging material."
With --planarizing sacrificial plugging material.--

Col. 10, line 53 -

Replace "node, the capacitor node, the first node, arid the second node."
With --node, the capacitor node, the first node, and the second node.--

Mailing Address of Sender:

Mark S. Matkin
Wells St. John P.S.
601 West First Avenue, Suite 1300
Spokane, WA 99201-3828

MAR 17 2006

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

Page 1 of 2

PATENT NO. : 6,921,692 *B2*
APPLICATION NO.: 10/615,287
DATED : July 26, 2005
INVENTOR(S) : Kunal R. Parekh, et al.

It is certified that an error or errors appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Cited Prior Art

In the second column, cover page, the 20th case cited:
Delete "6,458,859 B2 10/2002 Hayashida et al."
And insert --6,468,859 B2 10/2002 Hayashida et al.--

In the Specification

Col. 4, line 36 –
Replace "of example only, layer 54 might be formed to to provide a"
With --of example only, layer 54 might be formed to provide a--

Col. 4, line 40 –
Replace "layer 54 include conductive metal suicides (whether"
With --layer 54 include conductive metal silicides (whether--

In the Claims

Col. 7, line 17 -
Replace "The method of claim 13 wherein the sacrificial"
With --The method of claim 17 wherein the sacrificial--

Col. 7, line 60 -
Replace "plugging material with in the bit node contact opening"
With --plugging material within the bit node contact opening--

Mailing Address of Sender:
Mark S. Matkin
Wells St. John P.S.
601 West First Avenue, Suite 1300
Spokane, WA 99201-3828

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

Page 2 of 2

PATENT NO. : 6,921,692 *B2*
APPLICATION NO.: 10/615,287
DATED : July 26, 2005
INVENTOR(S) : Kunal R. Parekh, et al.

It is certified that an error or errors appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 7, line 66 -

Replace "The method of claim 23 wherein the forming or the"
With --The method of claim 23 wherein the forming of the--

Col. 9, line 52 -

Replace "plagiarizing sacrificial plugging material."
With --planarizing sacrificial plugging material.--

Col. 10, line 13 -

Replace "plagiarizing sacrificial plugging material."
With --planarizing sacrificial plugging material.--

Col. 10, line 27 -

Replace "outer material Selectively to the etch stop material."
With --outer material selectively to the etch stop material.--

Col. 10, line 47 -

Replace "plagiarizing sacrificial plugging material."
With --planarizing sacrificial plugging material.--

Col. 10, line 53 -

Replace "node, the capacitor node, the first node, arid the second node."
With --node, the capacitor node, the first node, and the second node.--

Mailing Address of Sender:

Mark S. Matkin
Wells St. John P.S.
601 West First Avenue, Suite 1300
Spokane, WA 99201-3828

MAR 17 2006